



# BITS Pilani

Pilani | Dubai | Goa | Hyderabad | Mumbai  
Research & Innovation

## Research Fellow (to join as PhD Scholar)

**Short Title: Transfer Learning-Based Smart Structural Damage Assessment Controller**  
**Pilani Campus | Deadline: 07 Oct 2024 |**  
**Joining: At the earliest**

Date: 25 Sep, 2024

Applications are invited for **ONE** position of **Research Fellow\*** on project, “Development of Transfer Learning-Based Smart Structural Damage Assessment Controller for Autonomous Prediction of Damage Scenarios in Digital Twin-Generated Population of Civil Structures” under the supervision of **Prof. Shashank Gupta**, Dept. of CSIS, Pilani Campus, **Prof. Devendra G. Patil** Dept. of Mechanical Engg., Goa Campus and **Prof. S. N. Patel**, Dept. of Civil Engg, Pilani Campus .

\* The selected candidate is enraged to join PhD Program of BITS Pilani. Interested candidates check the eligibility criteria and qualification process of the PhD program of BITS Pilani (<http://www.bitsadmission.com/phmain.aspx>).

Scope of work	Essential Qualification	Desirable Qualification
The project broadly focusses on the Cyber Physical Systems that focuses on Structural Health Monitoring (SHM) of civil structures using computational intelligence techniques. The candidate is expected to perform experiments using computational modelling driven by AI/ML frameworks and Digital Twin (DT) simulations.	Candidate must hold a first-class M.E./ M.Tech. / MS / M.Sc. / B.Tech./B.E. in Computer. Science/IT/Electronics/Electrical or equivalent degree.	Good spoken English and experience in technical writing. Prior experience in AI/ML frameworks, Python, MATLAB, NI -LabView  Any prior cross-disciplinary experience

**Fellowship:** ₹37,000 - ₹42,000 per month (based on the year of PhD and performance)

**Duration:** As per BITS Pilani norms (<http://www.bitsadmission.com/phmain.aspx>)

**Place of work:** Dept. of CSIS, BITS Pilani, Pilani Campus, Rajasthan 333031

**Application process:** Please apply with **CV and Cover letter** (showing alignment and justification with the roles/responsibilities/requirements) using this form

- **Google form link:** <https://forms.gle/WGSDp6GeLesuRZ8T7>
- **Deadline Extended to: 07 October 2024**

Preliminary shortlisting will be based on resume and telephonic/audio-visual interview within a week of last date of application. For final interview, the candidate will be informed through e-mail/ Phone for interview. No TA/DA will be provided in case of personal interview. For more details, please contact:

**Prof. Shashank Gupta**  
**Department of Computer Science and Information Systems (CSIS)**  
**BITS Pilani, Pilani Campus, Rajasthan**  
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