NOVEMBER 1, 2023

9 :00 am to 10: 30 am   Breakfast and registration
10: 30 am to 11 :00 am Inauguration
11:00 am to 11: 45 am   Inaugural Talk
11:45 am to 12:00 pm   Tea break
12:00 pm to 12:40 pm   RF applications in accelerators
12:45 pm to 1: 25 pm   RF for space applications
1:30 pm to 2:15 pm   Lunch
2: 15 pm to 2: 55 pm   Filters for EW applications
3:00 pm to 3: 40 pm   MMICs for defence applications
3: 45 pm to 4:00 pm   Tea break
4 :00 pm to 5:30 pm   Simulation session by CST Studio

NOVEMBER 2, 2023

8: 30 am to 9: 30 am   Breakfast
9:30 am to 10:10am   Active Electronic Scanning Array
10:15 am to 10: 55 am   Antenna for EMI/EMC
11:00 am to 11:40 am   Antenna for EW applications
11: 45 am to 12:00 pm   Tea break
12:00 pm to 12:40 pm   Antenna for HPM applications
12:45 pm to 1:25 pm   Antenna for Space applications
1:30 pm to 2:15 pm   Lunch
2:15 pm to 2 :55 pm   Introduction to computational EM
3 :00 pm to 4:10 pm   Simulation session by Dassault
4 :15 pm to 6:00 pm   Networking/Interaction with
                         Dassault

NOVEMBER 3, 2023

8: 30 am to 9: 30 am   Breakfast
9:30 am to 10:10 am   Microwaves for Fusion
10:15 am to 10: 55 am   MWs for Intentional EMI
11: 00 am to 11:40 am   MWs for Biomedical App
11: 45 am to 12:00 pm   Tea Break
12:00 pm to 12:40 pm   mm and THz wave-I
12:45 pm to 1:25 pm   mm and THz wave-II
1:30 pm to 2:15 pm   Lunch
2:15pm to 2:55 pm   Talk from industry-I
3:00 pm to 3:40 pm   Talk from Industry-II
3:40 pm to 5: 00 pm   Valedictory session

*Tentative Schedule

REGISTRATION

Research Scholars/
PDFs/Project staff } Rs. 1,000/-+GST

Faculty   : Rs. 1,500/-+GST

Industry delegates: Rs. 2,500/-+GST

Registration fee includes access to all technical sessions, lunch, tea/snacks and a registration kit. Payment procedure details are provided on the event website. The number of seats are limited and are on a first come basis.

Registration link: https://tinyurl.com/empra2023

🌐 https://www.bits-pilani.ac.in/empra-2023

WHO SHOULD ATTEND

Research scholars, faculties and working professional working in all areas of RF and microwave engineering including antennas, active and passive devices, EMI/EMC techniques and MMICs. A limited number of M.E/M.Tech students who are highly motivated towards research will be considered.

IMPORTANT DATES

Registration Starts   : October 10, 2023
Registration Ends   : October 21, 2023
Confirmation of   : October 11 onwards
registration by organiser

ACCOMODATION

Limited shared accommodation on nominal charges are available in the student hostel on first come first serve basis. A few number of hotels are available in the vicinity of the campus for interested participants details of which may be obtained from the organiser on request.

VENUE

BITS-Pilani, Hyderabad Campus

📞 Workshop Secratariat
+91 040-66 303 878
hvdixit@hyderabad.bits-pilani.ac.in

EMPRA 2023

A WORKSHOP ON

ELECTROMAGNETICS AND MICROWAVE ENGINEERING

NOVEMBER 1 - 3, 2023

ORGANISED BY

LAMBDA
Laboratory for Antenna, Microwave and e-Beam Devices and Applications

DEPARTMENT OF EEE

BITS PILANI, HYDERABAD CAMPUS

&

3DS DASSAULT SYSTEMES

The 3DEXPERIENCE Company

IN ASSOCIATION WITH
IEEE MTT/AP/EMC SOCIETY STUDENT BRANCH, BITS-HYD

SUPPORTED BY

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Subhradeep Pal
Supradeepan

ABOUT EMPRA

EMPRA (Electromagnetics and Microwave Engineering: Pedagogy, Research Trends and Applications) is a workshop series curated by the RF and microwave group - 'LAMBDA' (Laboratory for Antenna, Microwave and e-Beam Devices and Applications) at BITS-Pilani, Hyderabad Campus. The first workshop was held in 2019 and focussed on imparting hands on training on microwave active circuit design. The second workshop in the series was held in 2020 to give an insight on pedagogy along with research trends and development in the country in various national labs and industries

ABOUT EMPRA 2023

The third workshop in the series- EMPRA 2023, would be co-organised with Dassault Systèmes and is meant to showcase the activities carried out in various R & D labs and industries in the country via short, focussed talks and interactions. Additionally, simulation sessions would be conducted by Dassault-CST Studio on the themes of the presented talks in order to give the participants an insight into modelling complex phenomena. **Participants will also get an opportunity to interact with the technical team of Simulia CST regarding their simulation problems and queries. All registered participants will also be issued a trial license for CST Studio.**

ABOUT BITS

Birla Institute of Technology & Science Pilani (BITS-Pilani) is an institution of international repute. Over the years, BITS-Pilani has provided the highest quality technical education to students from all over India and abroad admitted on the basis of merit. BITS-Pilani is ranked among the top engineering institutes of India. BITS is a multi-locational university with four campuses in India, at Pilani, Goa, Hyderabad, Mumbai and one international campus in Dubai (UAE). During its five decades of existence as a university, BITS-Pilani has established strong linkages with industries, R&D organizations and financial institutions through its university-industry linkage programmes.

ABOUT EEE DEPARTMENT

The Department of Electrical and Electronics Engineering at BITS Pilani, Hyderabad campus started at the inception of the campus in 2008 and has been offering B. E. Electrical and Electronics Engineering, B. E. Electronics and Communication Engineering, B. E. Electronics and Instrumentation Engineering, M. E. (Communication Engineering), M. E. (Microelectronics Engineering), M. E. (Embedded Systems) and Doctoral programs. The department has extensive laboratory and infrastructural facilities for teaching, training, research and development.

EMPRA
2023